# Changes to Commands and Properties

## Command Changes

|  |  |
| --- | --- |
| **Property Name** | **Comment** |
| add\_regbus\_controller | This command adds a Regbus Controller (RBC) with its RBM and optional Axi4s bridge to tunnel requests from masters in the NoC, or APBM| AXI4LM| IOSFSB| IOSFSB2APB interfaces to drive external requests into the RBC. |
| vifce\_prop | This command is used to set or view a named property of a particular Interface's virtual interface and an optional direction in/out, w.r.t the host. |
| reset\_mesh | This command resets the previously created mesh. |

## Default Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| cc\_axi4m\_acelite\_conversion | no | Deprecated. Only the bridge property is available. |
| rssb\_router | no | Deprecated. By default all routers are RSSB. |

## Mesh Property Changes

None

## Bridge Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| axi4m\_ar\_max\_beats | 256 | This property is used to set the limit on the burst length for reads. |
| axi4m\_aw\_max\_beats | 256 | This property is used to set the limit on the burst length for writes. |
| axi4s\_memory\_type | device | This property indicates the Memory Type fot the AXI slave as described in the AXI spec. |
| cmi\_rd\_req\_max\_credit\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rdcpl\_max\_credit\_error\_fatal | no | This property is used to control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_route\_lookup\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| cmi\_rsp\_max\_credit\_error\_fatal | no | This property is control whether the interrupt raise on read credit overflow will be fatal or non-fatal. |
| cmi\_unsupported\_opcode\_error\_fatal | no | This property is used to enable the read completion error type functionality in the CMI bridge. |
| host\_idi\_max\_outstanding\_requests | -1 | This property sets the number of IDI max outstanding requests. |
| host\_upi\_max\_outstanding\_requests | -1 | This property sets the number of UPI max outstanding requests. |
| idi\_c2u\_strap\_data\_header\_sep\_enable | no\_w\_pins | This property is used to enable or disable the C2U straps to support separation of header and data in the IDI bridge and also to control the presence of C2U strap pins on the boundary of the bridge wrapper. |
| idi\_c2u\_strap\_support\_stall\_enable | no\_w\_pins | This property is used to enable or disable the C2U straps to support stalls in the IDI bridge and also to control the presence of C2U strap pins on the boundary of the bridge wrapper. |
| idi\_c2u\_strap\_spare | 4 | This property is used to set the width of the C2U spare strap signals for this bridge. |
| idi\_u2c\_strap\_spare | 5 | This property is used to set the width of the U2C spare strap signals for this bridge. |

## Host Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| cc\_directory\_latency | 1 | This property specifies the latency of the coherency directory RAM array for the target CCC. |
| llc\_data\_ram\_latency | 2 | This property specifies the latency of the data array for this cache. |
| llc\_tag\_ram\_latency | 2 | This property specifies the latency of the tag array for this cache. |
| cc\_default\_snoop\_mode | n/a | This property sets the masters that this CCC will snoop by default. |
| enable\_fine\_grain\_clock\_gating | no | The property, when enabled, will increase fine grain clock gate efficiency, but at the cost of additional time taken within the cycle. |
| conv\_stall\_enable | no | This property is used to determine whether the converter will use tracker storage to store the command properties for requests being sent from the CMIM. |
| conv\_tracker\_storage\_enable | yes | This property is used to determine whether the converter will use tracker storage to store the command properties for requests being sent from the CMIM. |
| conv\_rsp\_fifo\_depth | 16 | This property is used to set depth of the response fifo in the IDI2CMI converter block. |
| conv\_parity\_enable | no | This property is used to enable enhanced parity on the converters. |
| conv\_enh\_parity\_enable | no | This property is used to enable enhanced parity on IDI2AXI or IDI2CMI converters. |

## Interface Property Changes

|  |  |  |
| --- | --- | --- |
| **Property Name** | **Default Value** | **Comment** |
| cmi\_supports\_interleaved\_response | no | For CMIM bridges, indicates whether the CMI VC can accept interleaved read data completion responses. |

## Link Property Changes

None

## Router Property Changes

None

## VC Property Changes

None

## CSB Storage Property Changes

None